

Sr./Staff Reliability and Failure Analysis Engineer

Navitas Semiconductor is a venture back startup creating the world's first & fastest integrated GaN Power ICs for Mobile phone, tablet & laptop fast chargers, wireless charging and many other products. Navitas was founded in 2013 and is located in the heart of El Segundo. We are currently seeking a Product Engineering leader for our product engineering activities in this exciting new field of GaN power ICs. In this role, reporting to the VP Engineering, you will work in a fast-paced environment to develop, characterize, qualify and production-release of the world's first GaN Power ICs. The ideal candidate will serve as both a strong individual contributor as well as team leader.

Title: Sr. Reliability and Failure Analysis Engineer

Reports to: Sr. Director Quality and Reliability Engineering

Job Location: El Segundo, CA

Job purpose: Navitas is looking to hire a hands-on engineer to be a part of the reliability and failure analysis team and work on cutting edge GaN based power IC technology

Key Job Responsibilities:

- Be integral part of New Product and technology qualification (device & package) of GaN power devices
- Perform hands-on testing: Debug reliability hardware, Bench characterization, electrical failure analysis, etc
- Perform in-depth data analysis: parametric shift vs stress time (shift analysis), design and process DOE analysis using tools such as JMP and Weibull++
- Interface with reliability lab engineers and operators on test planning, failure disposition and driving best practices
- Ownership of reliability test planning and logistics
- Manage communication with external reliability and failure analysis labs
- Contribute in weekly reliability updates to internal teams
- Interface with internal cross-functional teams (design, product engineering, applications, failure analysis) to drive product improvement

Required Background:

- Bachelor of Science in Electrical Engineering/Physics/Materials Science
 - o Masters degree preferred
- Minimum 5 years of work experience in Semiconductor reliability engineering (device and package reliability)
 - o Experience with power semiconductor and/or GaN products preferred
- Background in statistical data analysis using advanced tools.
 - o Experience with JMP/Weibull++ preferred
- Hands-on reliability testing experience is required
- Experience with JEDEC qualification standards (experience with AEC standards is a plus)
- PCB layout experience is preferred
- Hardware automation using Labview is preferred

- Proficient with Microsoft office – Excel, PowerPoint, and Word
- Ability to work in a dynamic environment and wear multiple hats
- Excellent communication skills